

REGISTRATION FORM

One Week
ISTE-BNY Sponsored FDP (Online Mode)
on
Generative AI - Prototype to Production

1. Name : _____

2. Designation : _____

3. Organization : _____

4. Correspondence Address: _____

5. Mobile No. : _____

6. Email Id. : _____

7. Qualification : _____

8. Teaching/Research Experience : _____

9. ISTE Membership : Yes / No

If Yes, Membership No. _____

Preference will be given to ISTE Life Members.

Signature of Applicant

Signature of Head of Institute

(Interested candidates may register to this programme using registration link given below and attach the duly signed registration form.)

Patron

Hon'ble Shri. Jagdishji Gupta

President,
Sipna Shikshan Prasarak Mandal, Amravati

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Dr. Vishwas T. Gaikwad

Head of Department,
Department of Electronics & Telecommunication Engineering

Coordinator

Dr. Ashwini V. Malviya

Associate Professor,
Department of Electronics & Telecommunication Engineering

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Prof. N. T. Gadre, Prof U. V. Ramekar,

Dr. E. G. Rajgure, Dr. V. A. Gulhane

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SIPNA COLLEGE OF ENGINEERING & TECHNOLOGY, AMRAVATI

An Autonomus Institute (NAAC A+)

Approved by AICTE, New Delhi and
DTE, Govt. of Maharashtra, Mumbai and
Affiliated to SGBAU, Amravati.

One Week

ISTE-BNY Sponsored FDP (Online Mode)

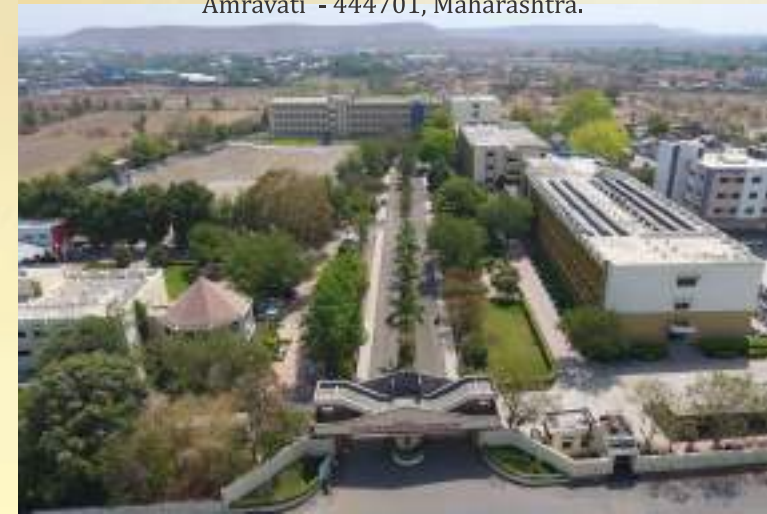
on

**Generative AI -
Prototype to Production**

29th June to 4th July 2026

Organized by
Department of
Electronics and Telecommunication Engineering
NBA Accredited

Sipna College of Engineering and Technology, Amravati
In Front of Nemani Godown, Badnera Road
Amravati - 444701, Maharashtra.



ABOUT INSTITUTE

Sipna College of Engineering & Technology, established in July 1999, located in Amravati is approved by the All-India Council for Technical Education (AICTE), New Delhi, and affiliated with Sant Gadge Baba Amravati University, Amravati. Sipna COET, an autonomous, unaided engineering institution is accredited by the National Assessment and Accreditation Council (NAAC) with an 'A+' grade and has five undergraduate programs accredited by the National Board of Accreditation (NBA). The institute is ISO 9001:2015 certified for its robust quality management system. Under the AICTE Margadarshan Scheme, Sipna COET has been recognized as a Mentee Institute by COEP Technological University, Pune. Additionally, the college is included under Sections 2(f) and 12(B) of the UGC Act, 1956. To strengthen industry-institute interaction and build a strong rapport with industry, the institute holds membership of the Confederation of Indian Industry (CII).

Strengthening its commitment to emerging technologies, Sipna COET proudly hosts Vidarbha's first DGCA-authorized Remote Pilot Training Organisation (RPTO), providing specialized training in drone technology and contributing to skill development in the rapidly growing aviation sector.

ABOUT DEPARTMENT

Established in 1999, the Department of Electronics and Telecommunication Engineering offers a B.Tech. program with an intake of 120 students and M.Tech. program (E&TC) with 18 seats. It has been accredited by the NBA for four consecutive terms of three years each, reflecting sustained academic quality and excellence. The department is supported by 31 faculty members, including 21 Ph.D. holders. The department is equipped with 16 state-of-the-art laboratories & Ph.D. research Labs approved by SGB

Amravati University, Amravati. The department has developed robust infrastructure through initiatives such as the AICTE-funded Microprocessor, Microcontroller, and Embedded Systems laboratory under the MODROB scheme, along with a Centre of Excellence established in collaboration with Pug Arch Technologies.

ABOUT THE PROGRAMME

As Generative AI shifts from experimental research to enterprise-grade production, the role of educators must evolve from theoretical instruction to architectural guidance. This FDP is designed to bridge the "implementation gap" by focussing on the Multi-Cloud Generative AI stack. While AWS and Azure provide the broader cloud context, the program centers on Google Cloud's Vertex AI as the primary engine for building, deploying and scaling agentic systems.

OBJECTIVES

Objectives of this FDP are as follows:

- **Architectural Mastery:** Enable faculty to build end-to-end Gen AI workflows using Vertex AI Pipelines and Cloud Run.
- **Platform Proficiency :** Build expertise in Google AI Studio and Firebase Studio for seamless AI prototyping to production.
- **Multimodal Development :** Master Vision API and Vertex AI ADK to build text, image, and video AI applications.
- **Cloud Interoperability :** To contextualize Google's offerings against AWS Bedrock and Azure Open AI, ensuring faculty can navigate and compare the industry's dominant ecosystems.
- **Responsible AI Integration:** To embed safety filters and ethical evaluation frameworks directly into the development lifecycle using Vertex AI's governance tools.

CERTIFICATION CRITERIA

To receive a certificate, participants must meet the following mandatory requirements:

- Attendance : Minimum 80% attendance for all sessions.
- Assessment : Minimum 60% score in the final assessment.

Resource Person:

Sessions will be led by recognized subject matter experts and eminent personalities from NITs, reputed institutes and industries.

Target Audience

Open to faculty members from engineering and technical institutions across multidisciplinary disciplines, with priority registration granted to ISTE Life Members.

Registration Details

- Mode: **ONLINE.**
- Registration Fee : There is **NO FEE** for participants.
- Participant Capacity : Limited to 50 participants with priority given to ISTE Life Members.

Registration Link

<https://forms.gle/2N4L7ZcJYAzVrTvq6>

