REGISTRATION FORM

**One-week ISTE Approved
Short Term Training Program
On
VLSI, Embedded System & IoT**

09/01/2023 to 13/01/2023

1. Name : _____

2. Gender : _____

3. Designation : _____

4. Organization :_____

5. Correspondence address : _____

_____

Mobile No : _____

E-mail : _____

6. Qualification : _____

7. Teaching/Research Exp. (Year) : _____

8. ISTE Membership No. : _____

9. Details of Payment : _____

Signature of   Signature of
Applicant   Head of Institute
with date   with date

Note: Photocopies of the form can be used for registration

Patron
Hon'ble Shri Jagdishbhau Gupta
President,
Sipna Shikshan Prasarak Mandal, Amravati (MS.)

Advisor
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Secretary,
Sipna Shikshan Prasarak Mandal, Amravati (MS.)

Organising Chair
Dr. Sanjay M. Kherde
Principal,
Sipna College of Engineering &Technology, Amravati

Convener
Dr. Avinash D. Gawande
Professor & Head,
Department of Electronics & Telecommunication
Engineering

Coordinator
Dr. Vishwas T. Gaikwad
Professor
Department of Electronics & Telecommunication
Engineering

Coordinator - MODROB Lab
Dr Nilesh N. Kasat
Professor
Department of Electronics & Telecommunication
Engineering

Organizing Committee Members
Dr. U. S. Ghate   **Dr. J. S. Edle**
Prof. N. T. Gadare   **Prof. U. V. Ramekar**
Dr. V. V. Rathi   **Prof. S. N. Kherde**

For Communication
Dr. U. S. Ghate, 9405173431
Prof. N. T. Gadare, 9096310166

Email : fdp.entc@sipnaengg.ac.in

**One week
ISTE Approved
Short Term Training Program
(Self-Finance)
On
VLSI, Embedded System & IoT**

Mon. 09 Jan. to Fri. 13 Jan. 2023

**Organized by
Department of
Electronics & Telecommunication
Engineering**

Ph.: 0721-2522342, 2522343, www.sipnaengg.ac.in
Email: fdp.entc@sipnaengg.ac.in

About the Institute

Sipna Shikshan Prasarak Mandal, established in the year 1995, aims at imparting higher & technical education to the people of the region. Since its inception, Sipna Shikshan Prasarak Mandal has been successfully running undergraduate and postgraduate courses in emerging areas to provide need based contemporary education.

Sipna College of Engineering & Technology was established in 1999 to provide quality professional & technical education to the students of the region and around, seeking various undergraduate, postgraduate engineering courses, management & research programmes. These courses are approved by AICTE, New Delhi, Government of Maharashtra and affiliated to Sant Gadge Baba Amravati University, Amravati (M.S.). National Assessment & Accreditation Council (NAAC) accreditation is another feather in the cap for the institution and it has received an 'A' Grade. Sipna College is mentee Institute of College of Engineering, Pune (COEP) under the Margdarshan Scheme of AICTE.

About the Department

The Department of Electronics and Telecommunication has 23 years of excellence in teaching and research. The Department offers UG, PG programs and Ph.D. research Lab approved by the SGB Amravati University. The department is Accredited by the NBA thrice. The highly qualified faculties are actively engaged in research and has published numerous papers in National and International Journals.

Objective of the Course

With the emerging technologies in the field of Engineering, it has become essential for academicians & professionals to become acquainted with development & advances in recent technologies. With this aspect, we are organizing **One week, ISTE Approved STTP on "VLSI, Embedded Systems and IoT"** on Self-Finance basis.

This program is specifically designed for the faculties in Engineering institutions, Polytechnics, to explore them with developments in the field of VLSI, Embedded Systems and IoT along with its applications.

Resource persons from industries & academic institutions of national repute, will deliver the sessions along with hands on practice in laboratory

MODROB Lab

At our institute, we are having AICTE Funded MODROB Lab at Department of Electronics & Telecommunications Engineering. As an academic institute we are facilitating this lab with licensed Cadence Software for VLSI, advance equipment for Embedded Systems and IOT. This lab can be accessed by UG, PG and Research students as well as staff of various colleges of the region.This Laboratory is facilitating for the skill enhancements of the students/ staff as per the need of industry.

Course Contents

- VLSI Design Flow
- CMOS IC Design
- Embedded System Design & Development
- Data Communication for Embedded Systems
- IoT & its Applications

Resource Persons :
The experts from Industry & Institutes of national repute will be the Resource Persons.

Eligibility :
Faculty members of Engineering Colleges/ Polytechnic Colleges and working professionals in the field of Computer, Electronics, Information Technology & relevant discipline are eligible to attend the course.

Important Dates :
- STTP Duration - 09/01/2023 to 13/01/2023
- Last Date for Registration - 02/01/2023

Certificates :
Participation Certificate will be issued to ISTE members from ISTE, New Delhi.
Non ISTE participant has to take the ISTE membership by paying the fee Rs. 3540/- (Incl. 18% GST) to the course coordinator at the spot.

Registration Details :
Registration Fees : Rs.1000/-for ISTE members only. (Registration fees includes registration kit, high tea)

Payment Mode :
Cash / DD / Cheque in Favour of "Principal, Sipna College of Engineering & Technology". Payable at Amravati.

Account details For Online Payment:
Bank Name: Axis Bank, Amravati
Account No:918020014279135
IFSC: UTIB0000265