

SIPNA COLLEGE OF ENGINEERING AND TECHNOLOGY, AMRAVATI
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING.

TIME-TABLE 2021-22 (ODD SEMESTER)

With Effect from : 23 / 08 / 2021

TIME	Sem	Section	11.00 to 11.45	11.55 to 12.40	12.40 to 1.00	1.00 to 1.45	1.55 to 2.40	
DAY			I	II		III		
MON	III	A	EMW(AAG)	ED&C(EGR)	RECESS	MIII(NDS)		
		B	EMW(AAG)	ED&C(NBS)		MIII(SMS)		
	V	A	CS(USG)	MC(NNK)		DSP(BSB)	Open Elective	
		B	CS(MAG)	MC(NNK)		DSP(NTG)	Open Elective	
	VII	A	SOFC(APT)	DIP(ASJ)		VLSI(SSM)	IMQC(SBM)	
		B	SOFC(APT)	DIP(ADG)		VLSI(VTG)	IMQC(SSJ)	
TUE	III	A	ED&C(EGR)	MIII(NDS)		DSD(JSE)	EVS(KVP)	
		B	ED&C(NBS)	MIII(SMS)		DSD(SNK)	EVS(KVP)	
	V	A	PE-I/FOC(SST/AVM)	DSP(BSB)		CS(USG)	Open Elective	
		B	PE-I/FOC(SST/AVM)	DSP(NTG)		CS(MAG)	Open Elective	
	VII	A	IMQC(SBM)	PE-I(SDJ)		SOFC(APT)		
		B	IMQC(SSJ)	PE-I(SDJ)		SOFC(APT)		
	WED	III	A	DSD(JSE)		EMW(AAG)	EVS(KVP)	OOP(RUV)
			B	DSD(SNK)		EMW(AAG)	EVS(KVP)	OOP(RUV)
V		A	CS(USG)	PE-I/FOC(SST/AVM)		MC(NNK)		
		B	CS(MAG)	PE-I/FOC(SST/AVM)		MC(NNK)		
VII		A	VLSI(SSM)	DIP(ASJ)		PE-I(SDJ)		
		B	VLSI(VTG)	DIP(ADG)		PE-I(SDJ)		
THU	III	A	MIII(NDS)	OOP(RUV)	EMW(AAG)			
		B	MIII(SMS)	OOP(RUV)	EMW(AAG)			
	V	A	MC(NNK)	DSP(BSB)	CS(USG)	Open Elective		
		B	MC(NNK)	DSP(NTG)	CS(MAG)	Open Elective		
	VII	A	DIP(ASJ)	SOFC(APT)	DIP(ASJ)	VLSI(SSM)		
		B	DIP(ADG)	SOFC(APT)	DIP(ADG)	VLSI(VTG)		
FRI	III	A	OOP(RUV)	EMW(AAG)	ED&C(EGR)	DSD(JSE)		
		B	OOP(RUV)	EMW(AAG)	ED&C(NBS)	DSD(SNK)		
	V	A	DSP(BSB)	MC(NNK)	PE-I/FOC(SST/AVM)	Open Elective		
		B	DSP(NTG)	MC(NNK)	PE-I/FOC(SST/AVM)	Open Elective		
	VII	A	SOFC(APT)	IMQC(SBM)	PE-I(SDJ)			
		B	SOFC(APT)	IMQC(SSJ)	PE-I(SDJ)			
SAT	III	A	MIII(NDS)	DSD(JSE)	OOP(RUV)	ED&C(EGR)		
		B	MIII(SMS)	DSD(SNK)	OOP(RUV)	ED&C(NBS)		
	V	A	CS(USG)	PE-I/FOC(SST/AVM)	MC(NNK)			
		B	CS(MAG)	PE-I/FOC(SST/AVM)	MC(NNK)			
	VII	A	VLSI(SSM)	PE-I(SDJ)	IMQC(SBM)			
		B	VLSI(VTG)	PE-I(SDJ)	IMQC(SSJ)			

Prof.S.D.Jirapure
Time Table Incharge

Dr.A.D.Gawande
HOD

Practicals :

Second Year

OOP : Computer LAB -1

ED&C: Communication Engg LAB

EWS : Instrumentation LAB

DSD : Digital electronics Lab

Third Year

DSP : Computer lab -2

**MC : Digital electronics and
microprocessor Lab**

PE : Power Electronics LAB

Electronics lab based on instrumentation : Instrumentation LAB

Final Year

VLSI LAB: Comp Lab 4

SDL-V LAB: Comp Lab 3